



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 9500

SERIAL NUMBER 10/811,768	FILING DATE 03/29/2004  RULE	CLASS 438	GROUP ART UNIT 2891	ATTORNEY DOCKET NO. CU-3664 RJS
-----------------------------	---------------------------------------	--------------	------------------------	---------------------------------------

## APPLICANTS

Kiyoshi Ooi, Nagano-shi, JAPAN;

Yasuyoshi Horikawa, Nagano-shi, JAPAN;  
Akio Rokugawa, Nagano-shi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

none CDW

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-104070 04/08/2003 CDW

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/08/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 23	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature			

## ADDRESS

26530  
LADAS & PARRY LLP  
224 SOUTH MICHIGAN AVENUE  
SUITE 1600  
CHICAGO, IL  
60604

## TITLE

Semiconductor device substrate, semiconductor device, and manufacturing method thereof

FILING FEE  RECEIVED 996	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
-----------------------------------	---	---